Claims

What is claimed is:

1. An isolating type self-oscillating flyback converter, including a coupled transformer (T), a FET (TR1), an oscillating transistor (TR2) and an optical-electro coupled isolating feedback unit (OP), wherein the input terminal of the circuit is connected to the source of the FET (TR1) through a primary winding (P1) of the coupled transformer (T), the input terminal of the circuit is connected to the collector of the transistor (TR2) through a resistor (R1) and another resistor (R2), the source of the FET (TR1) is connected to the collector of the transistor (TR2), one branch of the drain of the FET (TR1) is connected to the ground through a resistor (R4) and the other branch is connected to the base of the transistor (TR2) through the parallel connection unit of a resistor (R3) and a capacitor (C2), and the base of the transistor (TR2) is connected to the output terminal of a secondary output winding (P3) of the coupled transformer (T) through the electro-optical coupled isolating feedback unit (OP); the series connection joint (A) between the said resistor (R1) and the resistor (R2) is connected to the ground through a speedup capacitor (C1) and a secondary winding (P2) of the coupled transformer (T); **characterized in that** a loop for implementing the soft start of the circuit is connected between the said input terminal of the circuit and the series connection joint (A).

2. An isolating type self-oscillating flyback converter according to claim 1, **characterized in that** said soft start loop comprises the resistor (R1), a resistor (R5) and a capacitor (C3), said resistor (R5) is connected between the resistor (R1) and the series connection joint (A) in series, and one terminal of the capacitor (C3) is connected between the resistor (R5) and the resistor (R1), while another terminal is connected to the ground

3. An isolating type self-oscillating flyback converter according to claim 1, **characterized in that** said soft start loop comprises a resistor (R1) and a inductance (L), and said resistor (R1) and inductance (L) are connected between input terminal of the circuit and said series connection joint (A) in series.